

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application. No: 10/086,174				
Filed: February 28, 2002				
Inventor(s):				
Michael G. Lavelle, Ewa				
Kubalska & Yan Yan Tang				
Title: Active Block Write-Back				
from SRAM Cache to				
DRAM				
-				

		\mathbb{R}	ECEIVED
Examiner:	Chauhan, Ul		
Group/Art Unit:	2676		DEC 2 3 2004
Atty. Dkt. No:	5181-86900 P6060	Tech	nology Center 260
I hereby certify that this c the United States Postal S class mail in an envelor Patents, P.O. Box 1450, date indicated below.	ervice with sufficient pope addressed to Corr	ostage as imissioner	first r for

RESPONSE TO OFFICE ACTION OF SEPTEMBER 10, 2004

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of September 10, 2004, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.